

## **AMENDMENTS TO THE SPECIFICATION**

Please replace the second full paragraph on page 5 of the specification beginning after the reference numeral [0011] with the following paragraph:

-- However, in the conventional semiconductor integrated circuit as described above, the P-channel transistor in the clock buffer circuit is formed to be larger (usually on the order of two or three times larger) than the N-channel transistor, a gate capacity of the P-channel transistor increases. An input capacity of the clock buffer circuit thus increases.--